Online Parametric Identification of Power Impedances to improve Stability and Accuracy of Power-Hardware-in-the-Loop Simulations

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Abstract—This paper presents the Wideband System Identification (WSI) technique, i.e., an online method to identify power impedances over a wide frequency range from which the corresponding parametric impedance can be calculated online as well. The WSI technique exploits an existing custom 25 kW power electronic converter on the top of its power conversion function, which serves as power amplifier of an existing Power Hardware in the Loop (PHIL) simulation setup. The PHIL simulation technique allows connecting a real Device Under Test (DUT) with the real-time simulated Rest Of System (ROS) at power level. An Interface Algorithm (IA) on simulation side and a power amplifier (the 25 kW power electronic converter) connect ROS and DUT. The paper shows the impact of the uncertainties in the WSI chain on the accuracy of the impedance identification and highlights how the WSI technique can be combined with the Damping Impedance Method (DIM) IA to improve both accuracy and stability of the PHIL test bench. The application of the method is illustrated for the scenario of a PHIL test of a DC microgrid with a passive load.

Index Terms—impedance measurement; wideband impedance identification; power hardware in the loop; measurement uncertainty;

NOMENCLATURE

<table>
<thead>
<tr>
<th>ADC</th>
<th>Analog to Digital Converter</th>
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</thead>
<tbody>
<tr>
<td>DFT</td>
<td>Discrete Fourier Transform</td>
</tr>
<tr>
<td>DIM</td>
<td>Damping Impedance Method</td>
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<tr>
<td>DMA</td>
<td>Direct Memory Access</td>
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<tr>
<td>DUT</td>
<td>Device Under Test</td>
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<tr>
<td>FIFO</td>
<td>First In First Out</td>
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<tr>
<td>FlePS</td>
<td>Flexible Power Simulator</td>
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<tr>
<td>FPGA</td>
<td>Field Programmable Gate Array</td>
</tr>
<tr>
<td>IA</td>
<td>Interface Algorithm</td>
</tr>
<tr>
<td>IGBT</td>
<td>Insulated Gate Bipolar Transistor</td>
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<tr>
<td>NCS</td>
<td>Naturally Coupled System</td>
</tr>
<tr>
<td>PHIL</td>
<td>Power Hardware in the Loop</td>
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</table>

PRBS Pseudo Random Binary Sequence
RMSE Root Mean Square Error
ROS Rest Of System
RTDS Real-Time Digital Simulator
WSI Wideband System Identification

I. INTRODUCTION

In recent years, there has been an increasing interest in methods for wideband identification of power impedances [1]-[12]. These methods consist in generating a wideband perturbation controlled in both magnitude and duration for both voltage and current at the impedance measurement point. Then, advanced digital signal processing techniques can be applied on the measured voltage and current to identify the impedance of the system under test. Some of proposed solutions in the literature use dedicated power hardware (such as extra electronic amplifier or switching passive loads) to excite the wideband perturbation at power level which are recognized to be invasive methods [8]-[10], while some others use existing power hardware (such as existing power electronic converters or switching passive loads) which are recognized to be noninvasive methods [1]-[7],[11]-[12]. Among the noninvasive solutions, methods for wideband identification of power impedances in conjunction with existing power electronics converters are quite attractive, especially in modern power electronic distribution systems [2]-[7].

This paper presents a noninvasive online wideband impedance identification technique, called the Wideband System Identification (WSI) technique. It consists of injecting a short-time small-signal approximation of white noise (wide bandwidth in nature), i.e. Pseudo Random Binary Sequence (PRBS), on the control of an existing power converter while it is performing its power conversion function. Then, digital processing is performed in a dedicated control platform to identify the impedance of the connected device from the voltage and current measurements over the length of the injection. With respect to previous publications in the field of wideband measurement of power impedances [2]-[4], [8]-[12], the present work includes the online parametrization of the identified non-parametric impedance in the same control platform. The parametrization of the identified impedance is the key enabler.
for several system-level applications, such as system health monitoring, harmonic propagation detection, active filter monitoring, and adaptive control [1]-[12]. As a special application of the WSI technique, this paper presents how this type of measurement can improve the stability and accuracy of an existing Power Hardware in the Loop (PhIIL) simulation setup.

The PhIIL simulation technique allows testing a physical Device Under Test (DUT) connected to the real time simulated Rest Of System (ROS) through an Interface Algorithm (IA) and a power amplifier. This technique has been increasingly used by power system engineers to test power hardware [16]-[17], such as loads, converters, generators and protective devices, connected to complex power system models running in a real-time digital simulator. Classical advantages of PhIIL simulation techniques lay on the reduction of costs, risk, and time associated with each design cycle, and the increase of the safety of laboratory personnel. Despite their unquestionable advantages, PhIIL setups present challenges in terms of stability and accuracy, mainly due to the non-idealities of the power amplifier [18]. These non-idealities, e.g. delays, sensing errors, non-linearity, and limited bandwidth, prevent the power amplifier from tracking the reference accurately over a wide frequency band. The IA plays a key role to compensate for the shortcomings of the power amplifier and improves both the stability and the accuracy of PhIIL simulations. Several IAs are investigated and compared in [18]. Among them, the Damping Impedance Method (DIM) IA “is recommended for most PhIIL applications” [18], as it offers the best tradeoff between stability and accuracy. However, this method requires knowledge of the impedance of the DUT. The WSI technique is suggested in [19]-[21] as a method to identify the DUT impedance and it is here integrated in an existing PhIIL simulation setup to improve its stability and accuracy.

This paper extends previously presented work [7] by investigating the impact of the uncertainty components present in the chain of the WSI technique. In particular, it is shown how the uncertainty characteristics of the WSI tool and the resulting accuracy of the parametric impedance can play a key role for the stability of PhIIL test benches. Moreover, the benefits of the WSI to improve the accuracy and stability of an existing PhIIL setup are presented and discussed in detail.

II. THE WIDEBAND SYSTEM IDENTIFICATION TECHNIQUE

This section presents the online WSI technique exploiting the custom 25 kW power electronic converter, i.e. the power amplifier of the PhIIL setup called Flexible Power Simulator (FlePS) (fully described in Section III). The peculiarity of this technique is in how the perturbation is realized. Instead of performing a small-signal frequency sweep which requires long injection times, a short-time small-signal PRBS is used as test signal. PRBS is a digital approximation of white noise, therefore wideband in nature, and this allows creating a wideband perturbation at the impedance measurement point within a time window of ms.

Fig. 1 shows the conceptual block diagram of the implemented WSI technique. In practice, small-signal PRBS is added to the duty cycle and to both the current and voltage reference signals of the FlePS controller. The reason for such a choice is linked to the frequency response of the FlePS controller. If PRBS is injected to the duty cycle only, PRBS, seen as a disturbance, is rejected within the outer control bandwidth by effect of the nested control loops, while it is not rejected beyond the bandwidth of the outer control loop. On the other hand, if PRBS is injected to the current and voltage reference signals, it is not rejected within the current and voltage control bandwidths. Therefore, injecting PRBS to the duty cycle and to both the current and voltage reference signals ensures that PRBS is not rejected by the control action over a wide frequency range. Reference [6] contains some more details about the effect of the control loop to PRBS. Notice from Fig. 1 that, to obtain the desired controlled perturbation amplitude at the impedance measurement point, the PRBS signal needs to be properly scaled by the factors “a”, “b”, and “c” before being added to the duty cycle, current reference, and voltage reference, respectively.

The operating point of the DUT remains the same during the injection as the PRBS is a zero mean process and each value is uncorrelated with all the other values in the sequence [1]-[6]. Then, voltage and current measurements are processed in a dedicated LabVIEW Real-Time PC (RT-Target) that includes a NI RIO board (PCle-7851R) to identify the parametric impedance of the connected DUT.

Fig. 2 gives a schematic representation of the implemented
WSI technique. Notice that PRBS is generated within FlePS control platform as highlighted in Fig. 1. While the DUT is being perturbed, voltage and current at the terminals of the DUT are sampled with rate of up to 750 kS/s thanks the 16-bit Analog to Digital Converter (ADC) of the NI RIO board. The embedded FPGA (Virtex5) allows buffering the sampled values in a FIFO through a Direct Memory Access (DMA) channel. The subsequent processing routines are implemented on the CPU of the LabVIEW RT-Target. It is proven in [1]-[2] that the impedance of the DUT can be determined by dividing the Discrete Fourier Transform (DFT) of voltage \( v[n] \) and current \( i[n] \) samples.

\[
Z[e^{j\omega}] = \frac{\text{DFT}(v[n])}{\text{DFT}(i[n])}
\]  

(1)

This yields the non-parametric impedance consisting of \( N \) complex data points:

\[
N = t_{\text{window}} \cdot f_{\text{sampling}}
\]  

(2)

where \( t_{\text{window}} \) is the observation window during which PRBS is injected and voltage and current are measured. Then, the fitting routine online calculates the parametric transfer function from the non-parametric impedance. To impose equal fitting priority across all frequencies on the non-parametric data, a thinning technique is used to obtain a logarithmically spaced subset of the data points. This method reduces the computational time of the numerical least square fitting algorithm, which calculates the coefficients of the parametric impedance \( Z(s) \). The least square fitting requires as input the order of the numerator \( n \) and denominator \( m \). Finally, the proper parameters in terms of \( R_{\text{WSI}}, L_{\text{WSI}} \), and \( C_{\text{WSI}} \) are extracted from the parametric transfer function \( Z(s) \), and they will update the damping impedance (this latter step is explained in detail in Sections III and IV).

The execution time of the complete identification consists of the chosen time window and the impedance calculation time. The latter comprises the execution time of the DFT algorithm, which is of complexity \( O(N \cdot \log(N)) \), and of the fitting routine, which depends on exit conditions of the least-square fitting algorithm [25].

A. Overcoming Practical Challenges

A major practical challenge in impedance identification consists of maintaining the small-signal condition and, at the same time, guaranteeing a measurable perturbation within the frequency range of interest. In theory, the maximum identifiable frequency is defined by the Nyquist frequency [1]-[6], i.e. half the switching frequency of the power converter FlePS. In practice, the upper boundary depends on the amplitude of the injected PRBS signal. In fact, the output filter of the power converter attenuates the high frequency perturbation, eventually leading to a signal level that is comparable with the noise floor (\(< -60 \, \text{dB}\)) and therefore too small to be identified [1]. The lower frequency boundary, instead, is equal to the inverse of the measured time window \( t_{\text{window}} \) during which the PRBS is injected. As a consequence, the attenuation at high frequencies introduced by the output filter dictates the minimum amplitude of the injected PRBS signal. Therefore, to obtain a good identification as close as possible up to the Nyquist frequency, despite the attenuation of the output filter, and maintaining at the same time the small-signal condition, a good choice is to perturb voltage and current with magnitude between 5% and 10% of their steady-state value. As alternative to white noise, blue noise, which has a larger magnitude at high frequencies without affecting the amplitude at low frequencies, can be used. A blue noise filter can be applied to white noise PRBS to obtain a blue noise injection [2]-[3].

The superimposed perturbation signal has small amplitude with respect to the steady state voltage and current supplying the DUT and, therefore, likely to be noisy especially at high frequencies as explained before. Consequently, the ADC, necessary to measure voltage and current, plays also a key role for accurate identification. The chosen NI RIO board is equipped with a 16-bit ADC, which allows achieving a resolution of 0.305 mV on the maximum input analog voltage range of \( \pm10 \, \text{V} \). The minimum measurable voltage perturbation amplitude is given by 0.305 / 0.01185 = 25.74 mV, where 0.01185 is the voltage transducer gain. The minimum measurable current perturbation amplitude is given by 0.305 / 0.8 = 0.38 mA, where 0.8 V/A is the current transducer gain.

The choice of the time window is another practical challenge worthy of discussion. As the time window is directly linked to the number \( N \) of complex data points of the non-parametric impedance according to (2), for a fixed sampling rate, a long enough time window should be selected in order to have enough data points to capture eventual sharp features of the identified impedance, such as lightly damped resonances. On the other hand, the time window should be chosen short enough to avoid increasing impedance calculation times. Moreover, how short the time window should be chosen is in relation to the characteristics of the DUT. For active DUTs, such as power electronic converters, it is important to set the time window short enough in order to catch with fast changing impedances for example due to load steps. For highly nonlinear DTUs, such as diode bridges, reference [9] points out the need for a time window equal to half cycle of the nominal system frequency. This paper considers only passive power impedances as DUT and a practical choice is \( t_{\text{window}} = 100 \, \text{ms} \).

Another challenge is given when testing active systems with harmonic emission. By referring, for example, to the identification of \( Y \)-connected grid impedance described in [6], the presence of the fundamental frequency (50 Hz) on the active system under test (the grid itself) affects the accuracy of the non-parametric impedance identification around the system fundamental frequency, as depicted in Fig. 8 of [6], which shows three data points off around 50 Hz. To correct them, the voltage and current spectra acquired with and without PRBS, shown in Figs. 3-4, are subtracted as done in [6]. The corrected voltage and current spectra are depicted in Fig. 5, which shows the cancellation of the 50 Hz component. Since the cancellation is never perfect, it is proven in [6] that it is possible to improve the accuracy of the non-parametric impedance measurement by applying windowing (Hanning) to the DFT and performing synchronous acquisitions to ensure periodicity of the signal being processed by the DFT. The corrected non-parametric
impedance identification is depicted in Fig. 9 of [6], which shows that the three data points around 50 Hz have been reduced of about 20 dB in magnitude, while there is not significant improvement in phase. As the focus of this paper is the identification of passive impedances, the problem of existing harmonics is not present.

B. Wideband System Identification Uncertainty

As it will be shown in Section V, the reliable operation of the PHIL simulations can be strongly affected by the accuracy of the identified impedance parameters. For this reason, it is important to analyze the WSI chain and to estimate the uncertainty resulting due to the different uncertainty contributions. The computation of this uncertainty budget in analytical way is not easy because several hardware and software components (each one with its own uncertainty contribution) are in the WSI chain, and because a detailed characterization in the frequency domain of all these components would be needed. Last but not least, the random nature of the injected PRBS signal does not allow any a priori knowledge of the measured quantities characteristics, thus further complicating the scenario. The following uncertainty components affect the final WSI impedance results:

1) **Sensors uncertainties**: random errors given by current and voltage sensors are the first cause of uncertainty in the WSI chain; it is worth to remark that, because of the added white noise, both voltage and current have multiple tones which can be characterized by different uncertainties (depending on the sensor characteristics) both in magnitude and phase-angle.

2) **ADC uncertainties**: as known in the literature (see for example [15]), the analog to digital conversion stage is responsible of uncertainties due to the number of bits used in the quantization process, the time jitter and the microprocessor wordlength. Also in this case, a different impact can result for the tones at different frequencies and for magnitude and phase-angles.

3) **DFT uncertainties**: the DFT algorithm is not only responsible for the propagation of the uncertainties to the spectrum components, but it also brings additional uncertainties due to the leakage of inter-harmonic components. Windowing solutions can be used to mitigate the problem, but the random nature of the PRBS signals prevents the use of more advanced techniques to fully compensate for leakage errors.

4) **Impedance calculation**: as shown in (1), the non-parametric impedance is obtained through the division of voltage and current spectrum components. This step leads to the propagation of the uncertainties present in the voltage and current tones to the corresponding impedance components.

5) **Least Square Fitting**: an additional uncertainty contribution is brought by the iterative algorithm used to fit the impedance tones to the parametric model assumed for the impedance. Here both the fitting routine and the chosen model are sources of uncertainties.

6) **Impedance parameters calculation**: the resulting uncertainties of the coefficients of the transfer function (obtained as output from the fitting algorithm) are eventually propagated to the final impedance parameters.
III. POWER HARDWARE IN THE LOOP – INTERFACE ALGORITHM

This section presents the PHIL implementation with DIM IA and explains the motivation for identifying the DUT impedance. The presented scenario consists of a PHIL setup emulating a voltage divider, i.e., the Naturally Coupled System (NCS), as shown in Fig. 6(a). In this representation, the voltage source $u_1$ and the impedance $Z_A$ are part of the ROS, while the voltage source $u_2$ and the impedance $Z_B$ are part of the DUT. To build the PHIL system, the NCS is split into the ROS and the DUT. Fig. 6(b) shows the same system as PHIL setup with DIM IA. The power amplifier is modeled as a voltage-controlled voltage source with the feed forward transfer function $T_{FF}$, which models the amplifier limited bandwidth, and impedance $Z_{AB}$, which models the amplifier output impedance. The voltage reference $v_{A_{DIM}}$ is sent from the real-time simulator to the power amplifier. At the same time, current $i_{B_{DIM}}$ and voltage $v_{B_{DIM}}$ are sensed and fed back to be included in the real-time simulation. The transfer functions $T_D$ and $T_{FB}$ model the time delay introduced by the real-time simulator and the sensing gain, respectively. The impedance $Z^*$ is the damping impedance of the DIM IA. The open loop transfer function of the PHIL system with DIM IA ($G_{OL_{DIM}}$) is derived from the equivalent block diagram shown in Fig. 6(c) [20]:

$$G_{OL_{DIM}} = \frac{Z_A (Z_B - Z^*)}{(Z_{ab} + Z_B)(Z_A + Z_{ab} + Z^*)} T_{D} T_{FF} T_{FB}$$

which shows that, if the following condition is achieved,

$$Z^* = Z_B$$

$G_{OL_{DIM}}$ becomes zero and, therefore, never crosses the $-1$ point in the Nyquist diagram, thus, guaranteeing absolute stability.

As done in [20]-[21], considerations about the accuracy of the PHIL system can be formulated, by evaluating voltages and currents of the PHIL system with DIM IA on the simulation side (“A” subscript) and on the DUT side (“B” subscript) compared to those of the NCS picked as test case reference. The equations in Table I are derived by inspection of Fig. 6(a) for the NCS and of Fig. 6(c) for the PHIL system for the simpler case when $u_2 = 0$. The accuracy improvements of the PHIL system with DIM IA are evident when the following conditions are both true:

$$Z_{AB} = 0 \text{ and } Z^* = Z_B$$

The right column of Table I shows that the voltage and current at the ROS side perfectly match those ones of the NCS, while the performance at the DUT side is mainly determined by the bandwidth of the power amplifier. The considerations above about stability and accuracy improvements of PHIL experiments with DIM IA lead to the need to precisely identify the impedance of the DUT through the WSI tool presented in Section II. The Damping Impedance $Z^*$ can be updated while...

Fig. 6. (a) Voltage divider as NCS; (b) correspondent PHIL setup with MDIM IA; (c) equivalent block diagram of PHIL system with DIM IA.
the real-time simulation is running (see Fig. 6(b) in red), so that stability and accuracy of the PHIL system are guaranteed over a wide range of operating conditions.

<table>
<thead>
<tr>
<th>NCS</th>
<th>PHIL: DIM IA</th>
<th>PHIL: MDIM IA when $Z_{\omega} = 0$, $Z^* = Z_s$, $\omega = 0$, $\omega = \omega_0$</th>
</tr>
</thead>
<tbody>
<tr>
<td>$v_{A,NCS}$</td>
<td>$Z_B \cdot u_1$</td>
<td>$v_{A,\text{DIM}} = \frac{Z_A + Z_s}{Z_A + Z_B} \cdot v_{A,NCS}$</td>
</tr>
<tr>
<td>$v_{B,NCS}$</td>
<td>$v_{A,NCS}$</td>
<td>$v_{B,\text{DIM}} = T_{FF,\text{DIM}} \cdot v_{A,NCS}$</td>
</tr>
<tr>
<td>$i_{A,NCS}$</td>
<td>$\frac{u_1}{Z_A + Z_B}$</td>
<td>$i_{A,\text{DIM}} = \frac{Z_B}{Z_A + Z_B}$</td>
</tr>
<tr>
<td>$i_{B,NCS}$</td>
<td>$i_{A,NCS}$</td>
<td>$i_{B,\text{DIM}} = T_{FF,\text{DIM}} \cdot i_{A,NCS}$</td>
</tr>
</tbody>
</table>

### IV. Experimental Setup

The experimental setup of the PHIL test bench is depicted in Fig. 7. It also highlights in red the addition of the WSI tool. The WSI tool performs two tasks: first, it online identifies the DUT parametric impedance, and then it sends the identified parameters to the Real-Time Digital Simulator (RTDS) where the damping impedance $Z^*$ is updated.

#### A. Real-Time Digital Simulator (RTDS)

RTDS runs the models of ROS and IA in real time. Here, the hardware and software by RTDS Technologies Inc. is utilized, with a fixed time-step of 50 μs. At each time step RTDS calculates the voltage reference for the power amplifier, while at the same time the simulation receives the measured voltage and current from FlePS sensing unit. At the same time, the Damping Impedance $Z^*$ can be updated while the PHIL experiment is running.

#### B. Power Amplifier (FlePS)

The power part of the PHIL setup is the Flexible Power Simulator (FlePS) [22]-[23]. The power section of FlePS is depicted in Fig. 8. It consists of a three-phase isolating transformer, an active front end, and a DC link. Two of the four available IGBT legs form a DC/DC full bridge topology. The LC output filter with small damping resistors is set to realize a voltage source converter. This gives the opportunity to utilize a unipolar PWM modulation, which further reduces the switching ripple by doubling the apparent switching frequency to 80 kHz. A control scheme is implemented consisting of an inner deadbeat inductor current loop with an outer voltage PI controller. The crossover frequency of the voltage loop is 500 Hz. This means that the output of the power amplifier cannot track fast transients of the reference signal coming from RTDS.

#### C. Communication Interfaces

Fig. 7(b) shows the communication interfaces of the PHIL test bench, including information about the protocol and data type, as well as showing which information is being exchanged. The analog interface between RTDS and FlePS allows the signal exchanges at the Software/Hardware interface of Fig. 6(b). The digital interface between the NI LabVIEW RT Target and FlePS allows the synchronization of the PRBS injection with the data acquisition for the WSI technique. A dedicated FPGA (GT FPGA) bridges the data exchange between the RT Target and RTDS, which is used to update the damping impedance $Z^*$ of the DIM IA. The identified values of the passive load, i.e. R, L, and C, are directly available in RTDS and included in the DIM IA.

#### D. Device Under Test

The DUT is a flexible passive load rack that can be connected in various configurations. Here, single-phase RL and RLC loads are chosen as depicted in Fig. 9, described by their transfer functions (6) and (7).

$$Z_{BL} = R + s \cdot L$$  \hspace{1cm} (6)
\[ Z_{RLC} = \frac{R + s \cdot L}{1 + s \cdot RC + s^2 \cdot LC} \] (7)

Fig. 9. Passive DUT configurations.

V. RESULTS AND DISCUSSION

This section presents the determination of the performance of the impedance measurement via WSI tool and the propagation of its uncertainty onto the accuracy and stability of the PHIL experimental setup.

A. Wideband System Identification of DUT

As shown in Section II, the WSI technique allows identifying the non-parametric impedance of the DUT and translating it in its parametric form. The uncertainties brought and propagated during the different steps of the WSI result in an overall uncertainty for the identified impedance parameters. While the uncertainties given by sensors and ADC can be computed analytically following an ad hoc characterization of the used hardware, it is challenging to evaluate the uncertainties arising due to the software processing. For this reason, the uncertainty effects given by DFT and least squares fitting algorithm have been assessed here through off-line simulation tests.

Fig. 10 shows the results obtained through the DFT for the non-parametric RLC DUT impedance when: 1) no uncertainty is considered in the magnitude and phase-angles of current and voltage tones, and 2) 5\% uncertainty is considered for the magnitude of each current and voltage tone of the signal. In this test, white noise is emulated generating 10000 voltage components (overlapped to the 50Hz signal) with random frequency, amplitude and phase-angle and calculating the resulting current flowing through the DUT impedance. It is possible to observe that when no uncertainty is considered, the estimated impedance has a very good matching with the true one. Such result points out that the errors brought by the leakage in the DFT are in this case almost not propagated to the impedance when applying the division in (1). On the other side, when the inputs are uncertain, this compensation does not hold any more and both leakage and input signal errors are clearly propagated to the impedance.

Using the same test set-up, Table II shows the propagation of the uncertainty to the final impedance parameters, including both DFT and fitting routine (test has been performed through a Monte Carlo simulation with 1000 trials). It is possible to observe that in this case, also when no uncertainty is considered, the small errors given by the DFT are translated in non-negligible uncertainties when applying the fitting algorithm. In general, as it could be expected, the uncertainty trend is increasing for larger values of input uncertainties.

In Figs. 11-12, the non-parametric impedance (black dots) and the fitted parametric impedance (red line) obtained in the online PHIL scenario are plotted and compared with the analytical transfer function of the DUT (blue line) which is determined by measuring each electrical component individually with a frequency response analyzer, i.e. a Model 350c from Venable Instruments, and applying (6) and (7). It is possible to observe that the identified non-parametric impedance presents a certain scattering of the values due to the unavoidable uncertainties coming from the upstream hardware components.

The parametric identified values have been statistically characterized through a Monte Carlo analysis (by means of

Table II. PARAMETERS UNCERTAINTY WITH DIFFERENT LEVELS OF INPUT SIGNAL UNCERTAINTY

<table>
<thead>
<tr>
<th>Amplitude uncertainty input signal</th>
<th>% Relative Uncertainty (3(\sigma))</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>R</td>
</tr>
<tr>
<td>0%</td>
<td>3.1</td>
</tr>
<tr>
<td>5%</td>
<td>4.5</td>
</tr>
<tr>
<td>10%</td>
<td>7.5</td>
</tr>
</tbody>
</table>

Fig. 10. Bode plot (magnitude only) of the non-parametric RLC DUT impedance (red) compared to the analytical model (blue).

Fig. 11. Bode plot of parametric RLC DUT impedance (red) compared to the counterpart non-parametric (black) and the analytical model (blue).
TABLE III
IDENTIFIED VS. MEASURED VALUES OF RLC LOAD

<table>
<thead>
<tr>
<th></th>
<th>R</th>
<th>L</th>
<th>C</th>
</tr>
</thead>
<tbody>
<tr>
<td>Datasheets</td>
<td>10 Ω ± 5%</td>
<td>4.5 mH ± 3%</td>
<td>12 µF ± 10%</td>
</tr>
<tr>
<td>Measured with</td>
<td>10.53 Ω ± 0.7%</td>
<td>4.61 mH ± 0.7%</td>
<td>11.57 µF ± 0.7%</td>
</tr>
<tr>
<td>Frequency</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Response</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Analyzer</td>
<td>Identified with</td>
<td>Identified non-parametric</td>
<td>Identified parametric</td>
</tr>
<tr>
<td>WSI technique</td>
<td>10.87 Ω ± 11.7%</td>
<td>4.48 mH ± 16.5%</td>
<td>11.93 µF ± 11.0%</td>
</tr>
</tbody>
</table>

15000 online measurements) and compared with the measured values given by the frequency analyzer from Venable Instruments (Table III). Results show that the WSI values are in line with those provided by the frequency analyzer and the datasheets of the components (a slightly larger resistance has been found, but this can be given by additional contact resistances). Table III also shows that the uncertainty of the online WSI technique is heavily affected by the contribution of all the possible uncertainties components (here including also phase-angles uncertainties, that have not been considered previously, which can be significant at high frequencies, for example due to time jitter and the sensing instruments) and thus it results in higher values.

It is worth noting that the obtained uncertainty values are in any case a tradeoff with the chosen identification process, which has been designed to provide fast responses. The complete identification process of the results in Figs. 11-12 takes 110 ms, of which 100 ms are the chosen acquisition time window and the remaining 10 ms are needed for the impedance calculation (DFT and fitting routine).

B. PHIL improvements via online WSI technique

This subsection presents the experimental results of the application of the online WSI technique to the PHIL experiment. The governing question is whether the updating of the Damping Impedance $Z^*$ of the DIM IA results in improvements in accuracy and stability of PHIL experiment, thus validating (3) and the equations in Table I. Moreover, it is discussed how the accuracy of the DUT impedance identification given by the WSI technique impacts the stability and accuracy of the PHIL experiment. The ROS is modeled as an RL impedance with $10 \Omega$ and 5 mH. The results for the DUT configured as RL load were presented in [7], the results for the RLC load are presented in this paper.

Two cases, both using the DIM IA, but with different Damping Impedances are compared to analyze the stability margins improvement. The first case uses a rough estimate of the DUT impedance, i.e. purely resistive 5 Ω. This Damping Impedance guarantees the stable steady-state operating point needed to start up with the PHIL experiment. Then, the DUT impedance is identified to be an RLC load with $R_{WSI}=11.04 \Omega$, $L_{WSI}=4.40 \text{mH}$, and $C_{WSI}=11.99 \mu\text{F}$, configured as shown in Fig. 9. As last step, the damping impedance $Z^*$ is updated with these values.

1) Frequency Domain – Stability

A frequency response analyzer, i.e. a Model 350c from Venable Instruments, is used to plot the Nyquist diagram of the PHIL loop gain via loop injection technique [24]. The open loop gains of the PHIL setup without WSI (blue) and with WSI (red) are plotted in Fig. 13. The stability margins for both systems are included in cyan (phase margin) and yellow (gain margin). Both systems exhibit closed loop stability. Without WSI (blue) the

Fig. 12. Bode plot of parametric RL DUT impedance (red) compared to the counterpart non-parametric (black) and the analytical model (blue).

Fig. 13. Nyquist plot of the open loop gain of experimental PHIL setup comparing stability margins without (blue) and with WSI (red).

Fig. 14. Nyquist plot of the open loop gain with WSI parameters affected by large uncertainty.
PHIL setup displays a phase margin of 28.9° with a gain margin of roughly 1 dB. For the PHIL system with updated Damping Impedance (red), the first thing that becomes apparent is the large circle spanned by its open loop gain. To illustrate the complete curve, a zoomed-out graph is displayed on the top right corner of Fig. 13. The reason for this large amplitude is that the impedances of ROS and DUT form a resonant circuit. In the time domain, this becomes apparent by an oscillatory transient behavior. The stability margins for both PHIL systems, without and with WSI, are listed in Table IV. By updating the Damping Impedance, the gain margin increases while the phase margin remains almost unchanged.

While both the previous cases were stable, possible issues can arise in case of large uncertainties in the identified impedances. Fig. 14 shows, as an example, the off-line simulation results obtained for the open loop gain when considering the identification of the impedance parameters affected by a large uncertainty. To this purpose 100 tests have been performed by randomly extracting the impedance parameters to simulate a 40% uncertainty. Results show that, in this scenario, such an uncertainty could bring to cases of instability in the system. This emphasizes the importance of having an accurate identification of the DUT impedance and of having a suitable characterization of the associated uncertainties in order to avoid possible stability issues during the PHIL simulation. In the presented scenario, for example, an accurate identification of the impedance parameters is mandatory to ensure the stability of the PHIL simulation.

2) Time Domain – Accuracy

The controlled voltage source in the ROS is ramped up, such that the DUT voltage rises with a slope of 50 V/ms from 60 V to 110 V. Voltage and current waveforms on the ROS side and DUT side are compared during the transients to the equivalent ones of the NCS (the reference) running in the same simulation environment.

For the case without WSI, the voltage (top) and current (bottom) waveforms of the NCS (black), ROS (red), and DUT (blue) are depicted in Fig. 15. The voltage and current waveform of the NCS exhibit oscillations, which neither ROS nor DUT can correctly mimic since the Damping Impedance is purely resistive and therefore does not show the same transient behavior as the RLC load. The limited bandwidth of the power amplifier is apparent in both voltage and current waveforms as the blue curve trails the ROS with a delay of approximately 500 μs. Further, the large overshoot in current in the ROS (red) is due to the inaccurate Damping Impedance value.

After the WSI is performed and the Damping Impedance is updated, the same voltage ramp is applied again. The results depicted in Fig. 16 show a significant improvement in the dynamic accuracy. Both the voltage and current waveforms on the ROS side accurately follow the oscillatory transient of the NCS. On the DUT side, again, the limited bandwidth of the power amplifier prevents from tracking this oscillatory behavior. These two results are also predicted by the equations in Table I.

To assess the accuracy, the Root Mean Square Error (RMSE) is used in (8) as figure of merit. It quantifies the deviation between two waveforms in one single figure allowing a direct comparison between the system without WSI and with WSI.

\[
RMSE_{NCS,ROS} = \sqrt{\frac{\sum_{n=1}^{n}(V_{NCS,I} - V_{ROS,I})^2}{n}}
\]  

Table V quantifies the deviations of the voltage on the ROS and DUT sides to those of the NCS for both cases without and

| TABLE IV. STABILITY EVALUATION OF PHIL EXPERIMENT WITH RLC LOAD, COMPARE WITH AND WITHOUT WSI. |
|---------------------------------|-------------------|-------------------|
|                                | Without WSI       | With WSI          |
| Phase Margin                   | 28.9°             | 26.0°             |
| Gain Margin                    | 0.98 dB           | 2.58 dB           |

| TABLE V. ACCURACY EVALUATION COMPARING NCS AND PHIL (ROS AND DUT) WITHOUT AND WITH WSI |
|---------------------------------|-------------------|-------------------|
|                                | ROS               | DUT               |
| RMSE (to NCS)                  | 2.2 V             | 10.5 V            |
| RMSE                           | 0.7 V             | 9.7 V             |
with WSI by using (8). The RMSE on the ROS side improves dramatically from 2.2 V (without WSI) to as little as 0.7 V deviation with WSI. On the DUT side, the RMSE reduces by 0.8 V when updating the Damping Impedance.

To evaluate how the accuracy of the DUT impedance identification given by the WSI technique affects the accuracy of the PHIL experiment, simulations were run by considering increasing errors in the identified impedance. Fig. 17 shows the effect of four sub-optimal Damping Impedances whose RLC parameters are considered with negative errors equals to 0%, 10%, 20%, and 50%. The same voltage ramp-up of Figs. 15-16 is used. In line with the previous results, it is shown that only a very accurate identification of the DUT impedance (the Z*0% case in zoomed-in sections of Fig. 17) can guarantee voltage and current of the PHIL system on the simulation side to accurately follow the resonant oscillations of the NCS. Instead, in this scenario, voltages and currents on the DUT side of the PHIL system are not affected by these sub-optimal Damping Impedances in an appreciable way.

In conclusion, some considerations are made in regards to the possible applications of the online WSI technique coupled to a PHIL setup with DIM IA to test power hardware such as loads, converters, generators and protective devices, connected to complex power system models running in the real-time simulator. The case of a real static synchronous compensator (STATCOM) to provide voltage regulation through reactive power injection to the grid it is connected to is considered as an example. When building the PHIL system to test such an application, the real STATCOM is the DUT and it is connected to the grid model running in the real-time simulator through the IA and power amplifier. When testing such an application in PHIL with DIM IA, inaccurate knowledge of the STATCOM impedance over all operative conditions may lead to inaccurate results or even to instability. Therefore, to cope with this limitation, the online WSI technique can be particularly useful to provide the stability and accuracy improvements described thoroughly in this paper. Notice that the accuracy improvements described in this paper are only on the simulation side given the relatively low bandwidth of the power amplifier. If a power amplifier with higher bandwidth is used in the PHIL setup, the method of the online WSI technique coupled to a PHIL with DIM IA will provide accuracy improvements on the DUT side as well, as reported on the right column of Table I.

VI. CONCLUSIONS

This paper presented the accuracy and stability improvements achievable for PHIL simulations interfaced to a DIM IA employing online WSI technique. The implementation details of the WSI technique were presented, together with the hardware and software set-up needed for its integration into the PHIL platform. A qualitative evaluation of the different contributions to the overall uncertainty of the WSI chain has been provided and tests have been performed to assess the impact of input uncertainties on the post-processing routines. Tests demonstrated the capability of the WSI technique to correctly identify the impedance parameters by using very small time windows and without severe consequences on the uncertainty performance. The benefits provided by the presented technique are shown in the context of PHIL simulations, where identified impedances are used to online update the Damping Impedance used in the real-time simulator. The performed test cases using an RLC impedance as DUT in the PHIL system showed the clear improvements achievable in terms of both stability and accuracy allowed by the proposed implementation of the WSI technique.

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